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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Application Serial No. 09/420,635
Filing Date October 21, 1999
Inventor Werner Juengling
Assignee Micron Technology, Inc.
Group Art Unit 1746
Examiner Unknown
Attorney's Docket No. MI22-1243
Title: Semiconductor Processing Methods of Forming Devices on a Substrate,
Forming Device Arrays on a Substrate, Forming Conductive Lines on a
Substrate, and Forming Capacitor Arrays on a Substrate, and Integrated
Circuitry

Assistant Commissioner for Patents
Washington, D.C. 20231

ASSOCIATE POWER OF ATTORNEY

Please recognize Frederick M. Fliegel, Reg. No. 36,138; D. Brent Kenady, Reg.
No. 40,045; James E. Lake, Reg. No. 44,854; and Bernard Berman, Reg. No. 37,279;
whose post office address is 601 W. First Avenue, Suite 1300, Spokane,
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application.

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Date: 4/25/00

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